

Please substitute the paragraph starting at page 20, line 8 and ending at line 12, with the following paragraph. A marked-up copy of this paragraph, showing the changes made thereto, is attached.

AB -- $\Phi_{VR2}$  denotes the voltage applied to the gate of the transistor 8 for resetting the capacitance  $C_2$  of the output line 5 to reference potential  $V_{R2}$  and  $\Phi_{VR3}$  denotes the voltage applied to the gate of the transistor 11 for resetting the capacitance  $C_{SH}$  to reference potential  $V_{R3}$ .--

IN THE ABSTRACT:

Please amend the Abstract of the Disclosure to read as follows. A marked-up copy of the Abstract, showing the changes made thereto, is attached.

AM --An electromagnetic wave detector includes a conversion element for converting incident electromagnetic waves or high energy radiations into an electric charge, a storage capacitor for storing the electric charge produced by the conversion element, a thin film read transistor connected to the storage capacitor, and a thin film reset transistor also connected to the storage capacitor. To the gates of the read and reset thin film transistors are applied ON and OFF voltages at predetermined timings and these voltages are set to values such that any excessive electric charge produced in the storage period is discharged by way of the thin film reset transistor, not by way of the thin film read transistor, in the same storage period.--